

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	156	703/21.ccor.	US-PGPUB; USPAT	OR	ON	2005/04/25 18:24
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L6	826815	constraint coupling	US-PGPUB; USPAT	OR	ON	2005/04/25 18:24
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11.	((((((((pub-date > 1959 and pub-date < 2000 and FULL-TEXT((constraint or coupling)) and FULL-TEXT(interleav!)) and component) and distributed) and partition!) and allocat!) and software) and resource) and model!) and control) and segment) and latency [All Sources(- All Sciences -)]	15
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8.	((((((((pub-date > 1959 and pub-date < 2000 and FULL-TEXT((constraint or coupling)) and FULL-TEXT(interleav!)) and component) and distributed) and partition!) and allocat!) and software) and resource) and model! [All Sources(- All Sciences -)]	74
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 has the nature of a space partitioning algorithm **coupled** with backtracking. Furthermore, the algorithm
 a vector of integers, we encode each of the **component** integers with an identical number of bits. A
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process large numbers of examples, under the **constraint** of still achieving good accuracy. If e is the
 When there are no numeric attributes, C4.5, the **component** that induces decision trees, has complexity
 has already been induced. An alternative is to **interleave** the construction of all rules, evaluating each
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the system quiesces, the database consistency **constraint** is true. We described a two-level concurrency
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 The steps of concurrent transactions are **interleaved** at run time. Since locks are held for shorter
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[Interactive Resource Allocation by Problem Decomposition.. - Choueiry, Faltings \(1994\) \(Correct\) \(5 citations\)](#)

problem can be expressed as a discrete **Constraint** Satisfaction Problem (CSP)29]A **constraint**
 allocation problem into easy and difficult **components** interacting through abstracted pools of
 missing resources. We propose an algorithm that **interleaves** these processes. The main advantages of our
liaftp.epfl.ch/lia/Choueiry-93b.ps

[Proof Linking: Modular Verification of Mobile Programs in the.. - Fong, Cameron \(1999\) \(Correct\) \(4 citations\)](#)

we prefer strategies with fewer ordering **constraints** so long as the correctness conditions hold. 4
 particularly for classes with strong static **coupling** but weak dynamic **coupling**. The above analysis
 mobile code verifier as an individual engineering **component**, independent of the loading and linking
www.cs.sfu.ca/people/GradStudents/pwfong/personal/Pub/SFU-CMPT-TR-1999-02.ps

[Configurable, Mixed-Initiative Systems for Planning and.. - Stephen Smith \(1996\) \(Correct\) \(3 citations\)](#)

scheduling problems. This leads to a particular **constraint**-based solution framework, and a specific
 solving methods support only "over the wall" **coupling**, resulting in delayed detection of downstream
 on these ideas. We summarize basic architectural **components**, including libraries of **constraint** and domain
www.ri.cmu.edu/pub_files/pub1/smith_stephen_1996_1/smith_stephen_1996_1.ps.gz

[A three-tiered Confidence Model for Revising Logical Theories - Irene Weber \(1994\) \(Correct\) \(3 citations\)](#)

it consistent. Even if we use the additional **constraint** that the theory should be changed minimally,
 model is part of a theory specialization **component** of the modular inductive logic programming
 In contrast, incremental systems like MOBAL **interleave** construction and application of the theory.
www.informatik.uni-stuttgart.de/ifi/is/Personen/Irene/ilp94.ps.gz

[The Algebraic Specification Language LOTOS: An Industrial ... - Logrippo, Melanchuk, Wors \(1990\) \(Correct\) \(3 citations\)](#)

monolithic, state-oriented, resource-oriented and **constraint**-oriented. In monolithic style, t
 on algebraic concepts. LOTOS is made up of two **components**: a data type **component**, which is based, a on
 on al ates)parallel execution in **interleave**hide (hiding d [of gates)sequential
lotos.csi.uottawa.ca/ftp/pub/Lotos/Papers/formesode.90.ps.Z

[Deterministic expressions in C - Norrish \(1999\) \(Correct\) \(2 citations\)](#)

we have shown that the semantics' additional **constraints** actually result in a large class of C

To do this, Cholera keeps track of three state **components**: the pending side effects those parts issues such as whether or not function calls may **interleave** are debated. In addition to defining the www.cl.cam.ac.uk/users/mn200/research/.../PhD/deterministic-expressions.ps.gz

An Accelerated Interior Point Method Whose Running Time Depends .. - Vavasis, Ye (1993) (Correct) (1 citation)
of the feasible region defined by these **constraints**. The solid line is the central path, which is $O(j \log fflj)$ iterates. Note that ffl is a **component** of right-hand side vector c this explains why obtain a global minimum. In our new method, we **interleave** small steps with longer layered least squares ftp.cs.cornell.edu/pub/vavasis/papers/accel-ip.ps.Z

AutoFocus on Constraint Logic Programming - Lötzbeyer, Pretschner (2000) (Correct)
Proc. **Constraint**) Logic Programming and Software Engineering
share the commonality of synchronizing **components** via variables rather than explicit channels
www4.in.tum.de/~pretschn/private/.../papers/lpse00.ps.gz

Research Projects for Graduate Students - Frisch (Correct)
natural language processing, logic programming, **constraint** satisfaction and optimisation, inductive
Such methods are likely to form a vital **component** in the next generation of **constraint**
This project also will consider algorithms that **interleave** the generation of implied **constraints** with the
www-users.cs.york.ac.uk/~frisch/students-wanted.ps

Program Understanding: A Constraint Satisfaction Modeling.. - Woods, Yang, al. (1995) (Correct)
Program Understanding :A **Constraint** Satisfaction Modeling Framework Understanding
cs-archive.uwaterloo.ca/cs-archive/CS-95-52/CS95-52.ps.Z

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[Virtual Wires: Overcoming Pin Limitations in FPGA-based.. - Babb, Tessier, Agarwal \(1993\) \(Correct\) \(49 citations\)](#)
 greater that the available FPGA **resources**. This **constraint** yields the following possible scenarios for
 as nearest-neighbor) Nearest-neighbor topologies, **coupled** with the ability of virtual wires to overlap
 for these systems is considered the most complex **component** and comprises a major portion of system costs.
ftp.cag.lcs.mit.edu/virtual_wires/fccm93.ps.Z

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[A Computational Market Model for Distributed Configuration Design - Wellman \(1995\) \(Correct\) \(33 citations\)](#)
 parts that can perform that function. A set of **constraints**, R , dictating the allowable part combinations
 each agent is concerned with a subset of the **components** or functions of the artifact being designed,
 $\{C_i\}$, where C_i is a **partition** of P . A catalog design includes at most one
<linux.eecs.umich.edu/~people/wellman/aiedam95.ps.Z>

[A Resource-Based Prioritized Bisimulation for Real-Time Systems - Gerber, Lee \(1992\) \(Correct\) \(20 citations\)](#)
 a solitary event from one of the processes. This **constraint** quite naturally leads to an interleaving notion
 term may consist of at most one event from each **component resource**. Here, $p(\Sigma)$ denotes the set of
 R . This notion of execution leads to a natural **partition** of Σ into mutually disjoint
ftp.cs.umd.edu/pub/realtime/i_and_c.ps.gz

[System-Level Synthesis Using Evolutionary Algorithms - Blicke, Teich, Thiele \(1996\) \(Correct\) \(8 citations\)](#)
 set of implementations that satisfy a number of **constraints** on cost and performance. Existing
 operations with non-deterministic delays. Tightly **coupled** with the class of input specifications is the
 or [Gupta and Micheli, 1992] one programmable **component** and multiple hardware modules communicating
<ftp.tik.ee.ethz.ch/pub/people/thiele/paper/btt96a.ps.gz>

[Optimal Temporal Partitioning and Synthesis for Reconfigurable .. - Kaul, Vemuri \(1998\) \(Correct\) \(7 citations\)](#)
 on the FPGA devices) should be treated as a **constraint** which must be satisfied by every temporal
 logic blocks and function generators. We assume a **component** li- Heuristic temporal **partition** estimator
 Optimal Temporal **Partitioning** and Synthesis for Reconfigurable
<www.eecs.uc.edu/~ddell/projects/sparcs/Papers/date98.ps>

[Toward the Design Quality Evaluation of Object-Oriented.. - Abreu, Goulão, Esteves \(1995\) \(Correct\) \(6 citations\)](#)
 Factor (AIF) Polymorphism Factor (PF) **Coupling** Factor (CF) Each of those metrics refers to a
 with minor or major adaptations (from existing **component** libraries) must also be evaluated, along with
 alternatives are often available for the same **partition** of the system being modeled, design would
albertina.inesc.pt/ftp/pub/esw/mood/MoodPage/.../PAPERS/US_LETTER/icsq95.ps

[On the Role of Connectors in Modeling and Implementing.. - Oreizy, Rosenblum.. \(1998\) \(Correct\) \(4 citations\)](#)
 for the communications, and architectural **constraints** on **component** composition. On the Role of
 at the same time. Extensibility-The loose **coupling** between the **components** afforded by the
 organization of functionality into **components** and on the explicit representation and
<www.ics.uci.edu/~peyman/papers/TR-UCI-ICS-98-04.pdf>

[A Hardware-Software Cosynthesis Technique Based on Heterogeneous.. - Oh, Ha \(1999\) \(Correct\) \(3 citations\)](#)
 determine the optimal architecture within design **constraints**. However, there is a significant gap in the
 a significant gap in the way of dealing hardware **components**. An SOC research considers many implementation
 of each task should be selected. **Partitioning** and scheduling: **partition** the input tasks
<mirage.snu.ac.kr/publications/.papers/codes99.pdf>

[The NuMesh: A Modular, Scalable Communications Substrate - Ward, Abdalla, Dujari.. \(1993\) \(Correct\) \(3 citations\)](#)
 rigidly constrained and highly optimized. These **constraints** allow performance parameters for non-local

within heterogeneous networks. We view the **coupling** of communication substrates with processor to construct systems without redesigning every **component**. However, despite the wide acceptance of buses, <ftp.cag.lcs.mit.edu/pub/numesh/published/ics93.ps.Z>

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The Design of Eiffel Programs: Quantitative Evaluation.. - Abreu, Esteves, Goulão (1996) (Correct) (2 citations)
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Collective Problem Solving through Coordination in a Society of.. - Liu (1994) (Correct) (1 citation)
Abstract We present a methodology, called **Constraint Partition** and Coordinated Reaction (CP&CR) www.ri.cmu.edu/pub_files/pub1/liu_jyi_shane_1994_1/liu_jyi_shane_1994_1.ps.gz

Techniques for Automating Distributed Real-Time.. - Kang, Gerber.. (1999) (Correct)
In fact, meeting the end-to-end latency **constraints** is as important as throughput, since metrics are rechecked by simulation. The per-**component** load reservations can then be increased and the the overall process of parallelization and task **partitioning**. The scheduling synthesis problem is to find <ftp.cs.umd.edu/pub/realtime/hpdc.ps.gz>

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these local decisions maintain some system-wide **constraints**) We present several applications of such architectural style for distributed, loosely-**coupled**, heterogeneous software system development [20] for distributed control systems in which the **component** objects do not have access to the state of the <www.cs.caltech.edu/~adam/papers/gem/gem.ps>

Isaac: Building Simulations for Virtual Environments - Cremer, Vanecek (1994) (Correct)
formulate motion equations and kinematic **constraints**, and then numerically integrate them over kinematic **constraint** will be added. This equation **couples** the two previously independent sets of In the next section, we introduce the basic **components** of Isaac. Section 3 presents some historical <www.cs.uiowa.edu/~cremer/papers/ifip-proceedings.ps.gz>

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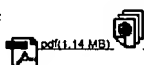
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